

PATENT APPLICATION
Docket No. 4591-170
Client No. IE10189-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jong-Ho Lee and Nae-In Lee

Serial No. 09/776,059

Examiner: Douglas Wille

Filed: February 2, 2001

Group Art Unit: 1773

For: DIELECTRIC LAYER FOR SEMICONDUCTOR DEVICE AND
METHOD OF MANUFACTURING THE SAME

Mail Stop NON FEE AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Responsive to the Restriction Requirement dated November 17, 2003, enclosed is an
amendment in the above-identified application.

The fee has been calculated as shown below.

CLAIMS AS AMENDED					
For:	Number After Amendment	Previous Number	Extra	Rate	Additional Fee
Total Claims	32	-45*	0	x \$18 =	\$0
Independent Claims	5	-6**	0	x \$86 =	\$0
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0

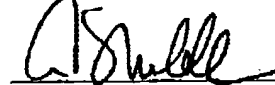
*greater of twenty (20) or number for which fee has been paid

**greater of three (3) or number for which fee has been paid

☒ Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Respectfully submitted,

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Group Art Unit 1773

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Date: December 17, 2003

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REMARKS:

Re: U.S. Patent Application No. 09/776,059
Our Ref. No. 4591-170

Enclosed is a response to the Restriction Requirement dated November 17, 2003.

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METHOD OF MANUFACTURING THE SAME**OFFICIAL**

MAIL STOP NON FEE AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Responsive to the Restriction requirement Action, dated November 17, 2003, please
amend the application as follows.

Amendments to the Claims are reflected in the listing of claims, which begins on
page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.

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